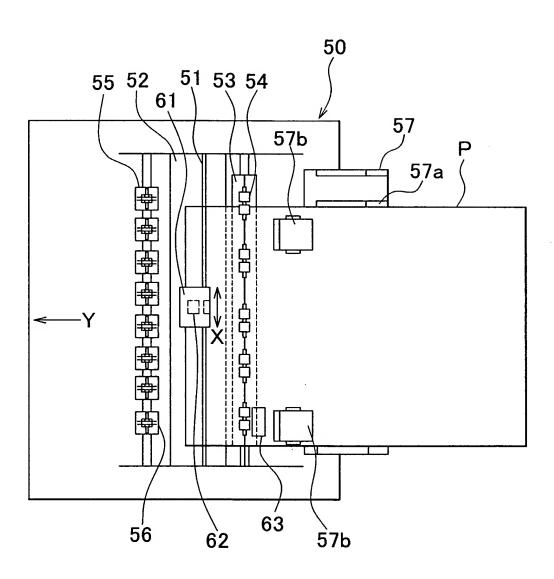
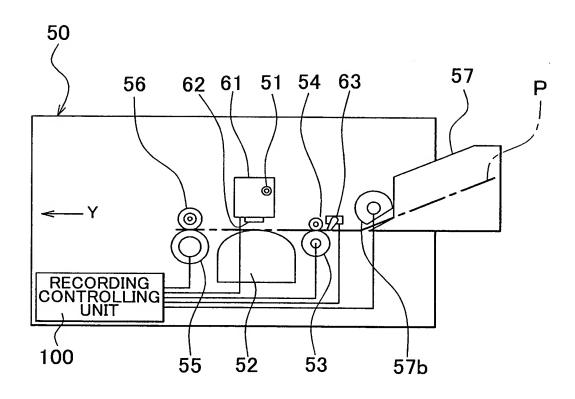


F I G. 1



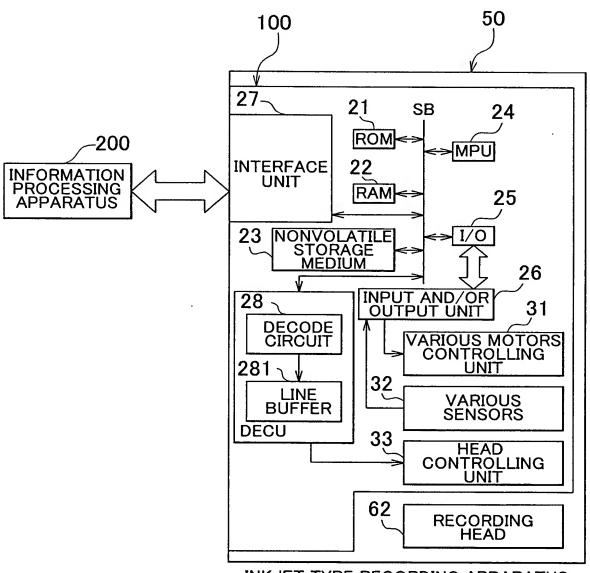


F I G. 2



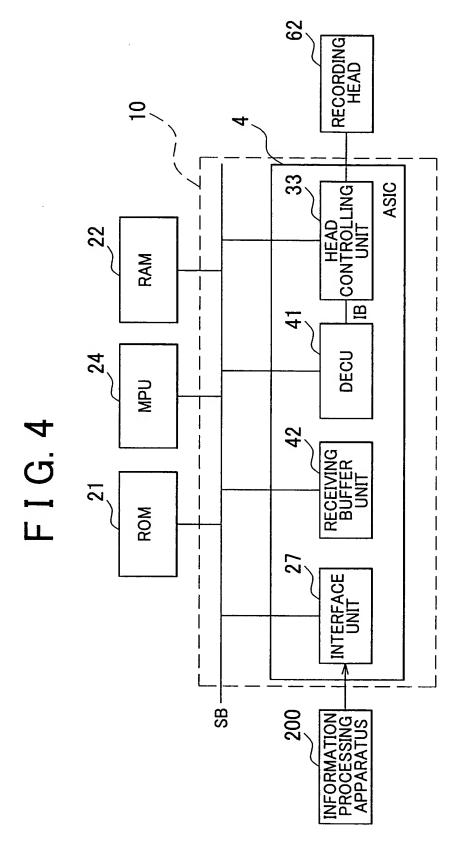


F I G. 3



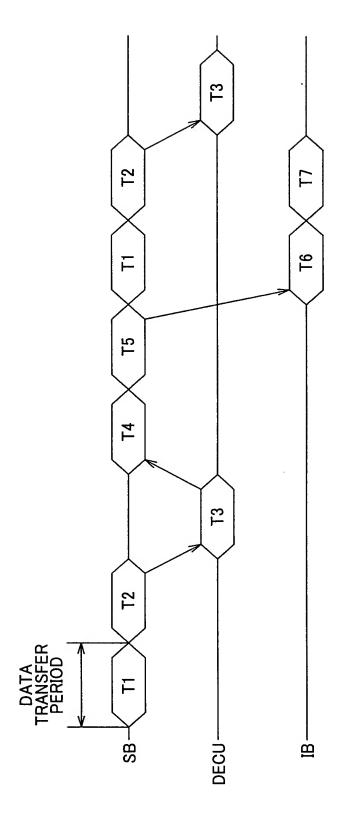
INKJET TYPE RECORDING APPARATUS





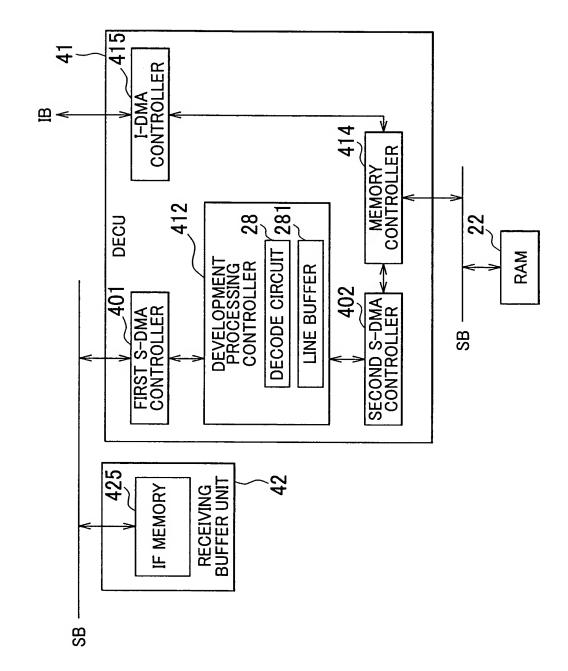


F I G. 5





F I G. 6





F I G. 7

OPERATION CONDITION IF MEMORY SIDE: STARTING ADDRESS OF RUN LENGTH DATA IS AN EVEN ADDRESS SYSTEM MEMORY SIDE: STARTING ADDRESS OF IMAGE DATA IS AN EVEN ADDRESS NUMBER OF BYTES IN 1 LINE: 16 BYTES

DECU TRANSFER SI FE 01 MEMORY FACE A 01 01 01 FE 01 FACE B 03 02 TRANSFER S2 03 02 78 55 FACE A 01 01 01 02 44 FB FACE B FF FE TRANSFER S3 78 55 01 01 01 02 78 55 11 06 FACE A 66 12 FACE B 77 45 TRANSFER S4 44 FB 89 10 FACE A 01 01 01 02 78 55 44 55 FB FACE B 10 FA TRANSFER S5 FF FE 20 08 FACE A 01 01 01 02 78 55 44 FF FF FF FF 12 13 FACE B 14 15 TRANSFER S6 11 06 TRANSFER D1 FACE A 01 01 01 02 78 55 44 FF FF FF FF FF 11 11 11 16 17 18 19 FACE B 20 FD TRANSFER S7 66 12 11 02 FACE A FACE B 66 12 98 B0 TRANSFER S8 77 45 F2 FC AB 03 FACE A FACE B 66 12 77 45 FF FE FC FD TRANSFER S9 89 10 FE FF FACE A FACE B 66 12 77 45 89 10 TRANSFER S10 55 FB FACE A FACE B 66 12 77 45 89 10 55 TRANSFER S11 10 FA FACE A FACE B 66 12 77 45 89 10 55 10 10 10 10 10 10 TRANSFER S12 20 08 TRANSFER D2 FACE A FACE B 66 12 77 45 89 10 55 10 10 10 10 10 10 20 20 20



F I G. 8

					•					
					•					
TRANSFER \$13 12 13										
	20 20	20 20	12 13	<u> </u>						į
FACE B	ļ	l		<u> </u>						j
TRANSFER S14 14 15	00.00	00.00	10.40		,	·				•
	20 20	20 20	12 13	14 15						ĺ
FACE B		L		l	L	L				j
TRANSFER S15 16 17	20.00	20.20	10 10	4446	14647					1
FACE A FACE B	20 20	20 20	12 13	14 15	16 17	<u> </u>				
TRANSFER S16 18 19		L		L	L	L	l			i
FACE A	20 20	20 20	12 13	14 15	16 17	18 19	· · · · · · · · · · · · · · · · · · ·			
FACE A	20 20	20 20	12 13	14 13	10 17	10 19				
TRANSFER S17 20 FD		L	L	L	L	L		L	نـــــا	i
FACE A	20 20	20 20	12 13	14 15	16 17	18 19	20			1
FACE B	20 20	20 20	12 13	14 13	10 17	10 19	20			
TRANSFER S18 11 02		L	l	L	·	L	L		TO	J ANSFER D3
FACE A	20 20	20 20	12 13	14 15	16 17	18 19	20 11	11 11	11	ANSFER D3
FACE B	11	20 20	12 10		10 17	10.10	20 11			
TRANSFER S19 98 BO		·	<u> </u>	L		L				, ,
FACE A		T		l	l	Γ				l
FACE B	11 98	В0								
TRANSFER S20 F2 FC	<u> </u>					<u> </u>	 			ı
FACE A						· ·				1
FACE B	11 98	B0 F2								
TRANSFER S21 AB 03										'
FACE A										ĺ
FACE B	11 98	B0 F2	AB AB	AB AB	AB					
TRANSFER S22 FF FE										_
FACE Â										İ
FACE B	11 98	B0 F2	AB AB	AB AB	AB FF	FE				j
TRANSFER S23 FC FD										_
FACE A	L				<u> </u>					į
FACE B	11 98	B0 F2	AB AB	AB AB	AB FF	FE FC	FD			į
TRANSFER S24 FE FF	ļ					,	,		TR	ANSFER D4
FACE A	1100	50.50	45.45	45.45	15 ==		CO C-			
FACE B	11 98	BU F2	IAR AB	IAR AB	IAB FF	IFE FC	FD FF	FF FF		

DECU



SETTING CONDITION NO VERTICAL LINE REARRANGEMENT

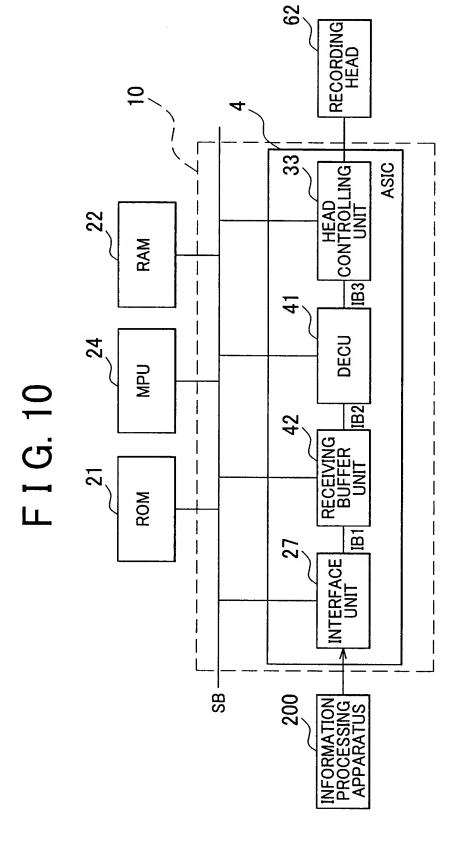
TOTAL NUMBER OF DEVELOPED BYTES: 64 BYTES(16 × 4)

NUMBER OF BYTES IN 1 LINE : 16BYTES NUMBER OF DEVELOPED LINES : 4 LINES

SYSTEM MEMORY

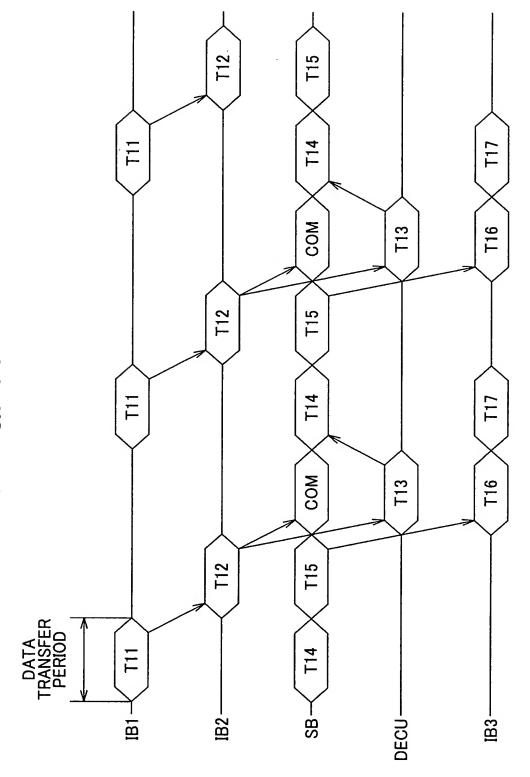
	SYSTEM MEMORY
FIG. 9A	D1— 01 01 01 02 78 55 44 FF FF FF FF FF FF 11 11 11 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
FIG. 9B	01 01 01 02 78 55 44 FF FF FF FF FF FF 11 11 11 11 02 12 77 45 89 10 55 10 10 10 10 10 10 20 20 20 00
FIG. 9C	01 01 01 02 78 55 44 FF FF FF FF FF FF 11 11 11 62 12 77 45 89 10 55 10 10 10 10 10 10 20 20 20 20 20 20 20 12 13 14 15 16 17 18 19 20 11 11 11 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00
FIG. 9D	01 01 01 02 78 55 44 FF FF FF FF FF FF 11 11 11 62 12 77 45 89 10 55 10 10 10 10 10 10 20 20 20 20 20 20 20 12 13 14 15 16 17 18 19 20 11 11 11 D4— 11 98 B0 F2 AB AB AB AB AB FF FE FC FD FF FF FF





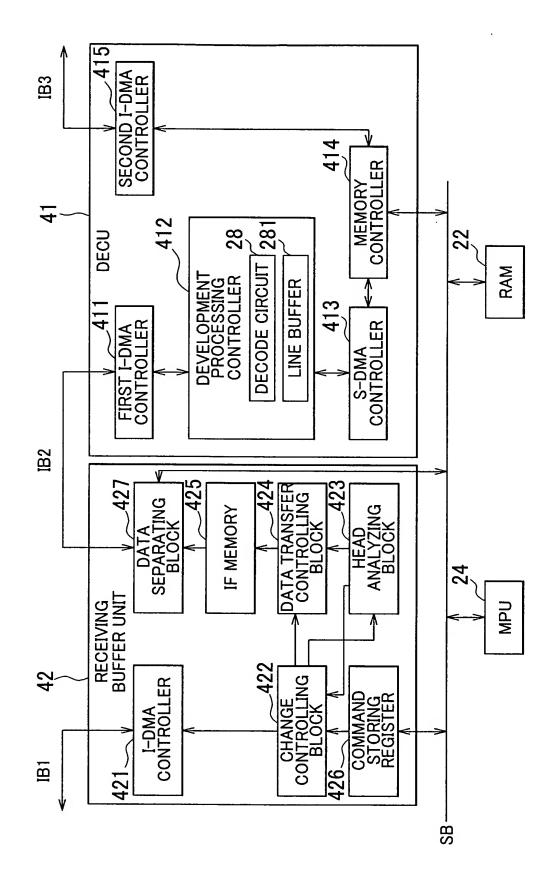






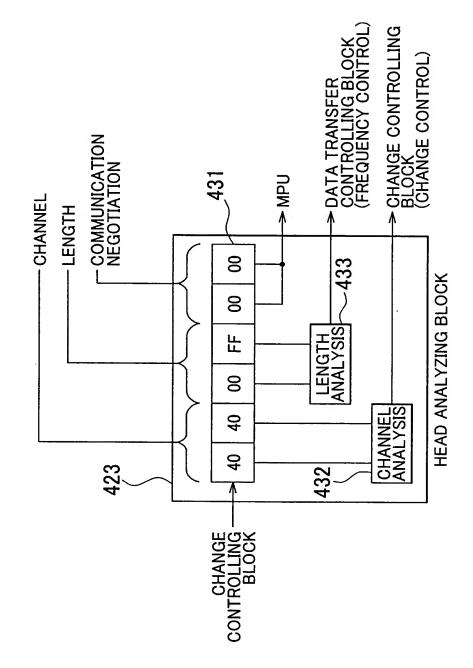


F I G. 12



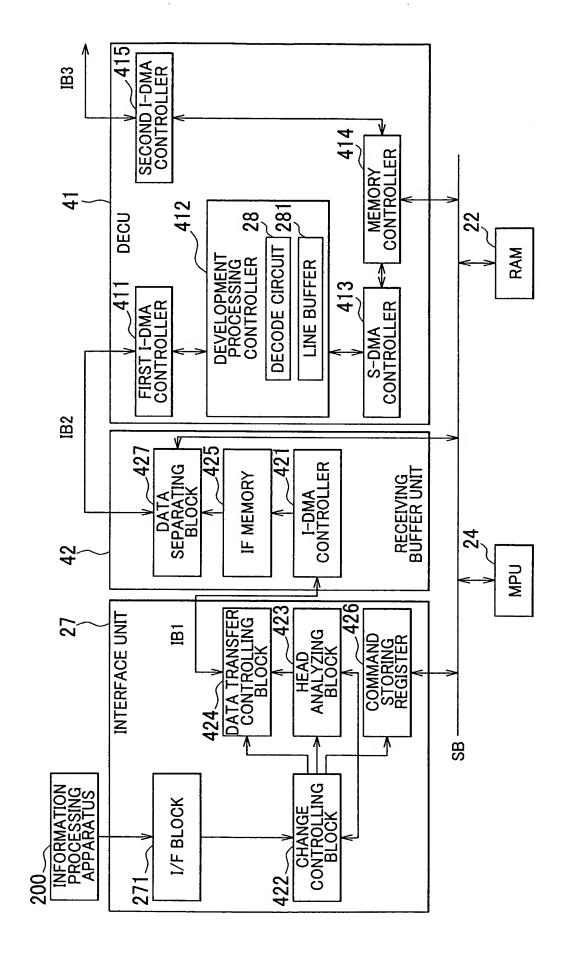


F I G. 13





F I G. 14





F I G. 15

